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1 Evaluation of closely coupled systems for high performance database processing
Rahm, E.;

Distributed Computing Systems, 1993., Proceedings the 13th International Conference on , 25-28 May 1993

Pages:301 - 310

[\[Abstract\]](#) [\[PDF Full-Text \(1136 KB\)\]](#) **IEEE CNF**
2 Representation of coherency classes for parallel systems
Hussak, W.; Keane, J.A.;

Parallel and Distributed Processing, 1993. Proceedings of the Fifth IEEE Symposium on , 1-4 Dec. 1993

Pages:391 - 398

[\[Abstract\]](#) [\[PDF Full-Text \(504 KB\)\]](#) **IEEE CNF**
3 Automating the assembly of presentations from multimedia database
Ozsoyoglu, G.; Hakkoymaz, V.; Kraft, J.D.;

Data Engineering, 1996. Proceedings of the Twelfth International Conference on , 26 Feb.-1 March 1996

Pages:593 - 601

[\[Abstract\]](#) [\[PDF Full-Text \(816 KB\)\]](#) **IEEE CNF**
4 Smart remote procedure calls: transparent treatment of remote poi
Kono, K.; Kato, K.; Masuda, T.;

Distributed Computing Systems, 1994., Proceedings of the 14th International Conference on , 21-24 June 1994

Pages:142 - 151

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1 Combining local and optimised power flow remedial measures in bus reliability assessment

Dornellas, C.; Schilling, M.; Melo, A.; Souza, J.C.S.; Do Coutto Filho, M.B.;
 Generation, Transmission and Distribution, IEE Proceedings- , Volume: 150 ,
 5 , 15 Sept. 2003
 Pages:629 - 634

[\[Abstract\]](#) [\[PDF Full-Text \(264 KB\)\]](#) **IEE JNL**

2 Steady-state security region (SSR) and its visualization of bulk high voltage power system

Xiaojun Zeng; Yixin Yu; Chunhua Huang;
 Power System Technology, 2002. Proceedings. PowerCon 2002. International Conference on , Volume: 2 , 13-17 Oct. 2002
 Pages:1259 - 1263 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(445 KB\)\]](#) **IEEE CNF**

3 Effect of voltage-loop controllers on bus voltage fluctuation and rec time in voltage source converters

Gitau, M.N.; Enslin, J.H.R.;
 Industrial Electronics, 1998. Proceedings. ISIE '98. IEEE International Symposium on , Volume: 1 , 7-10 July 1998
 Pages:208 - 213 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(600 KB\)\]](#) **IEEE CNF**

4 An optical bus-based distributed dynamic barrier mechanism

Cohen, W.E.; Hyde, D.W.; Gaede, R.K.;
 Computers, IEEE Transactions on , Volume: 49 , Issue: 12 , Dec. 2000

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1 The priority token bank: Integrated scheduling and admission control in an integrated-services network
Peha, J.M.;

 Communications, 1993. ICC 93. Geneva. Technical Program, Conference Record of the IEEE International Conference on , Volume: 1 , 23-26 May 1993
 Pages:345 - 351 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(812 KB\)\]](#) IEEE CNF

2 Compensating for moderate effective throughput at the desktop
Orphanos, G.; Birbas, A.; Petrellis, N.; Mountzouris, L.; Malataras, A.; Goldfarb, A.; Brosnan, L.; Janko, U.;

 Communications Magazine, IEEE , Volume: 38 , Issue: 4 , April 2000
 Pages:128 - 135

[\[Abstract\]](#) [\[PDF Full-Text \(64 KB\)\]](#) IEEE JNL

3 Design of deep sub-micron CMOS circuits
Joshi, R.; Roy, K.;

 VLSI Design, 2003. Proceedings. 16th International Conference on , 4-8 Jan. 2003
 Pages:15 - 16

[\[Abstract\]](#) [\[PDF Full-Text \(206 KB\)\]](#) IEEE CNF

4 The performance of TCP over ATM on lossy ADSL networks
Guang Lu; Simmonds, R.; Xiao Zhong; Unger, B.; Williamson, C.;

 Local Computer Networks, 2000. LCN 2000. Proceedings. 25th Annual IEEE Conference on , 8-10 Nov. 2000
 Pages:418 - 427



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- 1 Embedded applications: Architectural optimizations for low-power, real-time speech recognition 82%



Rajeev Krishna , Scott Mahlke , Todd Austin

Proceedings of the international conference on Compilers, architectures and synthesis for embedded systems October 2003

The proliferation of computing technology to low power domains such as hand-held devices has lead to increased interest in portable interface technologies, with particular interest in speech recognition. The computational demands of robust, large vocabulary speech recognition systems, however, are currently prohibitive for such low power devices. This work begins an exploration of domain specific characteristics of speech recognition that might be exploited to achieve the requisite performance

W ...

- 2 Performing remote operations efficiently on a local computer network 82%



Alfred Z. Spector

Communications of the ACM April 1982

Volume 25 Issue 4

A communication model is described that can serve as a basis for a highly efficient communication subsystem for local networks. The model contains a taxonomy of communication instructions that can be implemented efficiently and can be a good basis for interprocessor communication. These communication instructions, called remote references, cause an operation to be performed by a remote process and, optionally, cause a value to be returned. This paper also presents implementation considerations.

- 3 eNVy: a non-volatile, main memory storage system 82%

Michael Wu , Willy Zwaenepoel



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- 1**  **Mobile and Wireless System: Integration of dynamic voltage scaling and soft real-time scheduling for open mobile systems** 100%

Wanghong Yuan , Klara Nahrstedt

Proceedings of the 12th international workshop on Network and operating systems support for digital audio and video May 2002

Battery-powered mobile devices are becoming increasingly important computing platforms, which require low energy consumption while meeting the resource demands of a dynamic application workload. Most proposed dynamic voltage scaling (DVS) algorithms, targeting either best-effort or hard real-time systems, however, cannot be directly applied to such open mobile systems. This paper presents a framework to integrate DVS into soft real-time (SRT) scheduling for open mobile systems, achieving energy ...

- 2**  **Garbage collecting the Internet: a survey of distributed garbage collection** 100%

Saleh E. Abdullahi , Graem A. Ringwood

ACM Computing Surveys (CSUR) September 1998

Volume 30 Issue 3

Internet programming languages such as Java present new challenges to garbage-collection design. The spectrum of garbage-collection schema for linked structures distributed over a network are reviewed here. Distributed garbage collectors are classified first because they evolved from single-address-space collectors. This taxonomy is used as a framework to explore distribution issues: locality of action, communication overhead and indeterministic communication latency.

- 3** **Virtual machines: Memory resource management in VMware ESX** 100%



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1

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3

4

5

6

7

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1 Computing global virtual time in shared-memory multiprocessors **89%**



Richard M. Fujimoto , Maria Hybinette

ACM Transactions on Modeling and Computer Simulation (TOMACS) October 1997
Volume 7 Issue 4

Global virtual time (GVT) is used in the Time Warp synchronization mechanism to perform irrevocable operations such as I/O and to reclaim storage. Most existing algorithms for computing GVT assume a message-passing programming model. Here, GVT computation is examined in the context of a shared-memory model. We observe that computation of GVT is much simpler in shared-memory multiprocessors because these machines normally guarantee that no two processors will observe a set of memory operatio ...

2 Destructors, finalizers, and synchronization **88%**



Hans-J. Boehm

ACM SIGPLAN Notices , Proceedings of the 30th ACM SIGPLAN-SIGACT symposium on Principles of programming languages January 2003
Volume 38 Issue 1

We compare two different facilities for running cleanup actions for objects that are about to reach the end of their life. Destructors, such as we find in C++, are invoked synchronously when an object goes out of scope. They make it easier to implement cleanup actions for objects of well-known lifetime, especially in the presence of exceptions. Languages like Java[8], Modula-3[12], and C\#[6] provide a different kind of "finalization" facility: Cleanup methods may be run when the garbage collector ...

3 An effective garbage collection strategy for parallel programming **85%**



languages on large scale distributed-memory machines



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Prev
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1 Dynamic memory management for programmable devices 80%



Sanjeev Kumar , Kai Li

ACM SIGPLAN Notices , Proceedings of the third international symposium on Memory management June 2002

Volume 38 Issue 2 supplement

The paper presents the design and implementation of a novel dynamic memory-management scheme for ESP---a language for programmable devices. The firmware for programmable devices has to be fast and reliable. To support high performance, ESP provides an explicit memory-management interface that can be implemented efficiently. To ensure reliability, ESP uses a model checker to verify memory safety. The VMMC firmware is used as a case study to evaluate the effectiveness of this memory-management scheme ...

2 The architecture of a Linda coprocessor 80%



V. Krishnaswamy , S. Ahuja , N. Carriero , D. Gelernter

ACM SIGARCH Computer Architecture News , Proceedings of the 15th Annual International Symposium on Computer architecture May 1988

Volume 16 Issue 2

We describe the architecture of a coprocessor that supports the communication primitives of the Linda parallel programming environment in hardware. The coprocessor is a critical element in the architecture of the Linda Machine, an MIMD parallel processing system that is designed top down from the specifications of Linda. Communication in Linda programs takes place through a logically shared associative memory mechanism called tuple space. The Linda Machine, however, has no physically shared ...